



H-706-02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Reissue Patent Application of

K. ITOH et al

Serial No. 09/864,338

Group Art Unit: 2818

Filed: May 25, 2001

Examiner: S. Mai

For: LARGE SCALE INTEGRATED CIRCUIT WITH SENSE AMPLIFIER  
CIRCUITS FOR LOW VOLTAGE OPERATION

**SUPPLEMENTAL DECLARATION FOR REISSUE PATENT APPLICATION**

Assistant Commissioner of Patents

Washington, D.C. 20231

Sir:

As a below named inventor, I hereby declare that:

My citizenship, residence and post office address  
are as stated next to my name.

**Kiyoo Itoh**

Citizenship: Japan

Residence: Higashikurume, Japan

Post Office Address:

c/o Hitachi, Ltd. of Kanda Surugadai 4-chome,  
Chiyoda-ku, Tokyo, JAPAN,

**Yoshinobu Nakagome**

Citizenship: Japan

Residence: Hachioji, Japan

Post Office Address:

c/o Hitachi, Ltd. of Kanda Surugadai 4-chome,  
Chiyoda-ku, Tokyo, JAPAN,

I believe I am the original, first and joint inventor of the invention described and claimed in U.S. Letters Patent No. 5,526,313 and in the specification of the foregoing reissue application and for which improvement is solicited a reissue patent.

IDENTIFICATION OF ADDITIONAL ERROR

There is an error in the inventive entity set forth in the above-identified reissue application. Specifically, only the inventors Kiyoo Itoh and Yoshinobu Nakagome are inventors of the claimed subject matter of the present application.

More specifically:

(1) Applicants claim the benefit of priority of four Japan applications under 35 U.S.C. §119.

(2) Pending claims 53-74 are fully supported by priority application No. 01-29803, which only lists the above two inventors.

(3) By the present reissue application, the error is corrected by deleting the following inventors:

- 1) Jun Etoh;
- 2) Yoshiki Kawajiri;
- 3) Eiji Kume; and
- 4) Hitoshi Tanaka.

(4) The error in naming the additional inventors in the reissue application as originally filed arose without any deceptive intention on the part of the named inventors.

**ERROR STATEMENT**

Every error in the patent which was corrected in the present reissue application, and which is not covered by the prior declaration submitted November 30, 1998 in the parent application, arose without any deceptive intention on the part of the Applicants.

We hereby declare that all statements made herein of our own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

\_\_\_\_\_  
Date

\_\_\_\_\_  
Kiyoo Itoh

\_\_\_\_\_  
Date

\_\_\_\_\_  
Yoshinobu Nakagome